User Manual

TDS3TRG Advanced Trigger Application Module

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Contents

Safety Summary	2
Installing the Application Module	5
Advanced Trigger Features	5
Accessing Advanced Triggering	7
Advanced Trigger Concepts	8
Conventions	14
Pattern Triggering	15
State Triggering	17
Pulse Width Triggering	19
Runt Pulse Triggering	22
Slew Rate Triggering	25
Specifications	28

Safety Summary

To avoid potential hazards, use this product only as specified. While using this product, you may need to access other parts of the system. Read the *General Safety Summary* in other system manuals for warnings and cautions related to operating the system.

Preventing Electrostatic Damage



CAUTION. Electrostatic discharge (ESD) can damage components in the oscilloscope and its accessories. To prevent ESD, observe these precautions when directed to do so.

Use a Ground Strap. Wear a grounded antistatic wrist strap to discharge the static voltage from your body while installing or removing sensitive components.

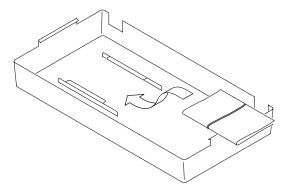
Use a Safe Work Area. Do not use any devices capable of generating or holding a static charge in the work area where you install or remove sensitive components. Avoid handling sensitive components in areas that have a floor or benchtop surface capable of generating a static charge.

Handle Components Carefully. Do not slide sensitive components over any surface. Do not touch exposed connector pins. Handle sensitive components as little as possible.

Transport and Store Carefully. Transport and store sensitive components in a static-protected bag or container.

Manual Storage

The oscilloscope front cover has a convenient place to store this manual.



Installing the Application Module

Please refer to the *TDS3000 & TDS3000B Series Application Module Installation Manual* for information on installing your application module.

Advanced Trigger Features

The TDS3TRG Advanced Trigger application module adds logic and pulse triggering capabilities to your TDS3000 series oscilloscope. This section provides an overview of these new features.

Logic Trigger Features

Logic triggering triggers the oscilloscope when two signals meet a Boolean logic condition. The advanced trigger module provides pattern and state logic trigger modes.

Pattern Trigger. Pattern triggering triggers the oscilloscope when two signals become logically true or false. Basically, the pattern-triggering feature triggers the oscilloscope from the output of a two-input AND, OR, NAND, or NOR logic gate. You can specify time constraints and signal threshold levels as part of the triggering condition. This trigger is useful for digital logic troubleshooting. **State Trigger.** State triggering triggers the oscilloscope when a state signal is true or false at the time a clock signal transition is true. This trigger is useful for trouble-shooting digital logic synchronous state machines.

Pulse Trigger Features

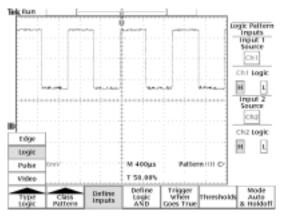
Pulse triggering triggers the oscilloscope when a signal meets a timing or threshold condition. The advanced trigger module provides three pulse trigger modes: pulse width, runt pulse, and slew rate.

Pulse Width. Pulse-width triggering triggers the oscilloscope when a signal pulse width is less than, greater than, equal to, or not equal to a specified pulse width. This trigger is useful for digital logic troubleshooting.

Runt Pulse. Runt-pulse triggering triggers the oscilloscope when a signal pulse is less than a specified threshold level. You can also specify runt pulse-width parameters. This trigger is useful for troubleshooting bus-contention problems. Slew Rate. Slew-rate triggering triggers the oscilloscope when a signal's slew rate (rise or fall time) is less than, greater than, equal to, or not equal to a specified slew rate. This trigger is useful for troubleshooting digital bus transceivers, transmission lines, and op-amp circuits.

Accessing Advanced Triggering

- 1. Push the Trigger **MENU** button to display the Trigger screen buttons.
- 2. Push the **Type** bottom screen button to display the trigger type pop-up menu.
- 3. Push the **Type** bottom screen button to select Logic or Pulse triggering.
- 4. Push the Class screen button to select trigger class.



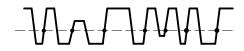
Advanced Trigger Concepts

This section introduces the concepts of signal logic and thresholds as they relate to advanced triggering. These concepts apply to most or all of the advanced trigger functions. You should read this section if you are not familiar with advanced triggering concepts or Boolean logic.

Overview

Edge triggering can trigger on most signals, and is the default trigger type. Edge triggering sets the oscilloscope to trigger (acquire signal data) when a signal meets a specified signal slope and a single voltage-threshold condition.

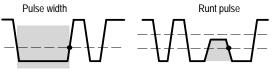
Single threshold voltage setting



• = Possible trigger points for positive slope signals

However, there are times when you need to trigger the oscilloscope on a more complex signal, or when two signals meet a condition, in order to troubleshoot a particular problem. These problems include a pulse that is too narrow or wide, and situations in which one signal is true when a second signal transitions from low to high.

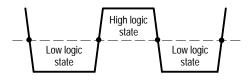
Advanced triggering can help acquire signals with these types of problems. Advanced triggering lets you further qualify the trigger conditions by adding parameters such as pulse width, delta time, logical comparisons of two signals, and dual threshold levels.



• = Trigger point

Thresholds

Both pulse and logic triggering trigger the oscilloscope when one or two signals are logically true. To determine whether a signal is true or false, you must set a signal reference point that determines whether a signal is in one of two states. You set this reference point by specifying a threshold voltage level for each trigger signal. Crossing the threshold level toggles the state value of that signal.



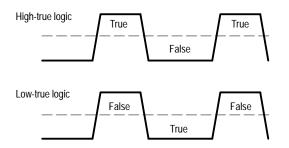
• = Transition point from one state to another

Logic State

The actual state (true or false) of a signal depends on how you define its signal logic setting, which can be either high-true or low-true. Defining a signal as hightrue (H) means that signal levels above (more positive than) the threshold level are true, and signal levels below (more negative than) the threshold level are false.

A low-true (L) logic setting is just the opposite. Defining a signal as low-true means that signal levels below (more negative than) the threshold level are true, and signal levels above (more positive than) the threshold level are false. Low logic effectively inverts the signal.

Defining the logical state of a signal lets you use Boolean logic to evaluate when a condition is true for two signals.



Boolean Logic

The signal logic (threshold level and high-true/low-true logic) defines which part of a waveform cycle is true or false. You then use Boolean logic to evaluate or compare the logic of two signals as part of a trigger condition.

The four logical comparison functions are AND, OR, NAND, and NOR:

- The AND function means that if both signal logic states are true, the condition is true, otherwise the condition is false.
- The OR function means if either or both signal logic states are true, the condition is true, otherwise the condition is false.

- The NAND (Not-AND) function means that if both signal logic states are true, the condition is false, otherwise the condition is true. This function is the inverse of the AND function.
- The NOR (Not-OR) function means if any or all of the trigger signal logic states are true, the condition is false, otherwise the condition is true. This function is the inverse of the OR function.

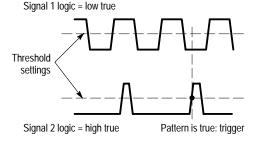
Remember that the logic function evaluates the logic states of two signals, and that the logic state of each signal depends on whether they are set to high-true or low-true logic.

For example, assume that you want to trigger the oscilloscope only when signal one is low at the same time that signal two is high. Therefore you want to:

- Set a threshold level that is appropriate for each signal.
- Set signal one to be true when it is low (low-true signal logic).

- Set signal two to be true when the signal is high (high-true signal logic).
- Trigger when both conditions are true (AND trigger logic).

Trigger logic: Signal 1 AND Signal 2



The material you have just read provides a basic understanding of the triggering concepts you need in order to use the Logic and Pattern triggering functions. Refer to the *Reference* section for detailed information about the advanced trigger functions.

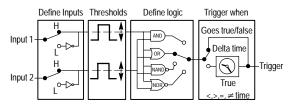
Conventions

The following conventions apply to all advanced trigger functions:

- You cannot use any of the advanced trigger functions to arm B triggering.
- You do not have to display a channel in order to use the channel as a trigger source.
- The range of time values for pulse width (regular and runt) and slew rate is from 39.6 ns to 10 s.
- In the menu tables, *N* represents a numeric value entered using the general purpose knob.

Pattern Triggering

Pattern Trigger Conditions



Pattern Trigger Menu

Table 1: Trigger Menu:	: Type = Logic,	Class = Pattern
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Bottom	Side	Description
Define Inputs	Input 1 Source	Sets the pattern trigger signal input 1 source.
	Logic	Sets the signal logic for input 1. H = high true, L = low true.
	Input 2 Source	Sets the pattern trigger signal input 2 source.
	Logic	Sets the signal logic for input 2. H = high true, L = low true.
Define Logic	and, or, Nand, Nor	Sets which logic function to apply to the input signals.

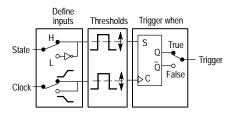
Table 1: Trigger Menu:	Type = Logic,	Class = Pattern (cont.)
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Bottom	Side	Description
Trigger When	Goes True/ Goes False	Triggers the oscilloscope when the logic condition is true or false.
	Is True < N	Triggers the oscilloscope when the input logic condition is true
	Is True > N	for a time period greater than or less than time period <i>N</i> .
	Is True = N	Triggers the oscilloscope when the input logic condition is true for a time period equal to or not equal to time period N within a $\pm 5\%$ tolerance.
	ls True ≠ N	
Thresholds	Level (Input 1) N	Sets the threshold voltage level for input 1 and 2 to level <i>N</i> ,
	Level (Input 2) N	using the general purpose knob.
	Set to TTL	Sets the threshold voltage level to 1.4 V for both inputs.
	Set to ECL	Sets the threshold voltage level to –1.3 V for both inputs.
	Set to 50%	Sets the threshold voltage level to 50% of each input's peak-to- peak value.
Mode & Holdoff		Same as Edge Trigger.

Trigger When. The input condition must be true or false for ≥ 2 ns in order for the oscilloscope to detect the pattern.

State Triggering

State Trigger Conditions



State Trigger Menu

Table 2: Trigger Menu: Type = Logic, Class = State

Bottom	Side	Description
Define Inputs	State Input Source	Sets the state signal source.
	Logic	Sets the signal logic for state input source. H = high true, L = low true.
	Clock Input Source	Sets the clock signal source.
	Slope	Sets the signal slope (rising or falling) for clock input. The clock slope defines when the clock signal is true.

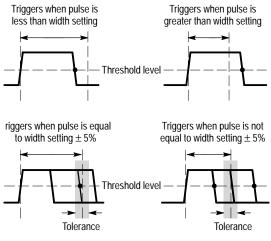
Table 2: Trigger Menu:	: Type = Logic,	Class = State (cont.)
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Bottom	Side	Description
Trigger When	Goes True	Triggers the oscilloscope if the state signal is true when the clock signal slope is true.
	Goes False	Triggers the oscilloscope if the state signal is false when the clock signal slope is true.
Thresholds	Level (State Input) N	Sets the threshold voltage level for state and clock signals to level <i>N</i> , using the general purpose knob.
	Level (Clock Input) N	
	Set to TTL	Sets the threshold voltage level to 1.4 V for both inputs.
	Set to ECL	Sets the threshold voltage level to –1.3 V for both inputs.
	Set to 50%	Sets the threshold voltage level to 50% of each input's peak-to- peak value.
Mode & Holdoff		Same as Edge Trigger.

Trigger When. The state signal must be true or false for ≥ 2 ns prior to the clock transition in order for the oscilloscope to detect the state.

Pulse Width Triggering

Pulse Trigger Conditions



• = Trigger point

Pulse Width Trigger Menu

Bottom	Side	Description
Source	Ch1 - Ch4	Sets the pulse width signal source.
	Ext	Sets external or external divided
	Ext/10	by 10 as the signal source.
	AC Line	Sets the AC line frequency as the trigger source. This trigger source is only available when the oscilloscope is connected to AC power.
	Vert	Sets the lowest-numbered displayed channel as the trigger source.
Polarity	Positive	Sets the source signal pulse
	Negative	polarity on which to trigger.
Trigger When	Pulse Width < N	Triggers the oscilloscope when the source signal pulse width is less than or greater than the specified pulse width <i>N</i> . Triggers the oscilloscope when the signal pulse width is equal to or not equal to the specified pulse width <i>N</i> within a ±5% tolerance.
	Pulse Width > N	
	Pulse Width = N	
	Pulse Width $\neq N$	

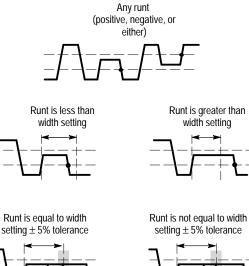
Table 3: Trigger Menu: Type = Pulse, Class = Width (cont.)

Bottom	Side	Description
Level	Level N	Sets the signal threshold volt- age level to <i>N</i> using the general purpose knob.
	Set to TTL	Sets the signal threshold volt- age level to 1.4 V.
	Set to ECL	Sets the signal threshold volt- age level to –1.3 V.
	Set to 50%	Sets the threshold voltage level to 50% of the signal's peak-to-peak value.
Mode & Holdoff		Same as Edge Trigger

Trigger When. The source pulse width must be ≥ 5 ns in order for the oscilloscope to detect the pulse.

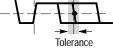
Runt Pulse Triggering

Trigger Conditions



→ <

Tolerance



• = Trigger point

Runt Pulse Trigger Menu

Table 4: Trigger Menu: Type = Pulse, Class = Runt

Bottom	Side	Description
Source	Ch1 - Ch4	Sets the runt signal source.
	Ext	Sets external or external divided
	Ext/10	by 10 as the signal source.
	AC Line	Same as description on
	Vert	page 20.
Polarity	Positive	Sets the source signal runt
	Negative	pulse polarity on which to
	Either	trigger.
Trigger When	Runt Occurs	Triggers the oscilloscope when any runt pulse is detected, regardless of width.
	Runt Width < N	Triggers the oscilloscope when the runt signal pulse width is
	Runt Width > <i>N</i>	less than or greater than the specified pulse width <i>N</i> .
	Runt Width = <i>N</i>	Triggers the oscilloscope when the runt signal pulse width is
	Runt Width $\neq N$	equal to or not equal to the specified pulse width <i>N</i> within a ±5% tolerance.

Bottom	Side	Description
Thresholds	High N	Sets the runt signal high thresh- old and low threshold voltage
	Low N	levels to value <i>N</i> , using the general purpose knob.
	Set to TTL	Sets runt signal threshold voltage levels to 2.0 V (high threshold) and 0.8 V (low threshold).
	Set to ECL	Sets runt signal threshold voltage levels to –1.1 V (high threshold) and –1.5 V (low threshold).
Mode & Holdoff		Same as Edge Trigger

Trigger When. The source runt pulse width must be ≥ 5 ns in order for the oscilloscope to detect the pulse.

Slew Rate Triggering

Slew Rate Trigger Conditions

Signal slew rate is greater Signal slew rate is less (slower (faster) than specified slew rate than specified slew rate Specified slew rate Specified slew rate Actual slew rate Actual slew rate Signal slew rate is equal Signal slew rate is not to specified slew rate equal to specified slew rate $\pm 5\%$ tolerance $\pm 5\%$ tolerance Tolerance Tolerance

• = Trigger point

Slew Rate Trigger Menu

Bottom	Side	Description
Source	Ch1 - Ch4	Sets the slew rate signal source.
	Ext	Sets external or external divided
	Ext/10	by 10 as the signal source.
	AC Line	Same as description on
	Vert	page 20.
Polarity	Positive	Sets the source signal slew rate
	Negative	polarity on which to trigger.
	Either	
Trigger When	Slew Rate < N	Triggers the oscilloscope when the signal slew rate is less than
	Slew Rate > N	or greater than the specified slew rate <i>N</i> .
	Slew Rate = N	Triggers the oscilloscope when the signal slew rate is equal to
	Slew Rate	or not equal to the specified slew rate <i>N</i> within a ±5% toler-
	≠ N	ance.
	Delta Time N	Shows the delta time compo- nent <i>N</i> of the slew rate, as set using the general purpose knob.

Table 5: Trigger Menu: Type = Pulse, Class = Slew Rate

Table 5: Trigger Menu: Type = Pulse, Class = Slew Rate (cont.)

Bottom	Side	Description	
Thresholds	High N	Sets the signal high threshold and low threshold voltage level	
	Low N	 components of the slew rate to value <i>N</i>, using the general purpose knob. 	
	Set to TTL	Sets the signal threshold volt- age levels to 2.0 V (high threshold) and 0.8 V (low threshold).	
	Set to ECL	Sets the signal threshold volt- age levels to -1.1 V (high threshold) and -1.5 V (low threshold).	
Mode & Holdoff		Same as Edge Trigger	

Delta Time and Thresholds. The delta time and threshold settings determine the calculated slew rate (volts ÷ time). Changing either value changes the calculated slew rate.

Trigger When. The delta time component of the slew rate (time from threshold to threshold) must be ≥ 5 ns in order for the oscilloscope to detect the slew rate.

Specifications

This section describes the TDS3TRG Advanced Trigger application module specifications. All specifications are guaranteed unless labeled "typical." Typical specifications are provided for your convenience but are not guaranteed.

To meet specifications, two conditions must first be met:

- The oscilloscope must have been operating continuously for ten minutes within the operating temperature range specified.
- You must perform the Compensate Signal Path operation described in the oscilloscope user manual. If the operating temperature changes by more than 10° C, you must perform the Compensate Signal Path operation again.

Table 6: TDS3TRG specifications

Characteristic	Description
Logic and Pulse Trigger Sensitiv- ity, typical	1.0 division at BNC, DC Coupled, \geq 10 mV/div to \leq 1 V/div (pattern, state, delay, width, and runt triggering)
Slew Rate Trig- ger Sensitivity, typical	Same as Edge Trigger Sensitivity specifica- tions in the oscilloscope user manual.

Table 6: TDS3TRG specifications (cont.)

Characteristic	Description		
Logic Triggering	Pattern	State	
Minimum Logic	2 ns	2 ns	
Time, typical	Pattern minimum logic time: the time that a logic pattern must be valid to be recognized. State minimum logic time: the time that a logic state must be valid before and after the clock edge to be recognized.		
Logic Triggering	Pattern	State	
Minimum Rearm	2 ns	4 ns	
Time, typical	Pattern minimum rearm time: the time that a logic pattern must be invalid before a new occurrence of the pattern is recognized. State minimum rearm time: the time between consecutive clocks.		
Pulse Triggering Minimum Pulse Width, typical	5 ns For pulse and runt, minimum pulse width refers to the pulse being measured. For slew rate, minimum pulse width means the minimum delta time that the oscilloscope recognizes.		

Table 6: TDS3TRG specifications (cont.)

Characteristic	Description	
Pulse Triggering Minimum Rearm Time, typical	5 ns For pulse and runt, rearm time refers to the time between measured pulses. For slew rate, rearm time refers to the time it takes the signal to recross the two signal thresholds.	
Delta Time Resolution using general purpose knob	Time Range	Resolution
	39.6 ns to 9.99 µs	13.2 ns
	10 µs to 99.9 µs	92.4 ns
	100 µs to 999 µs	1 µs
	1 ms to 9.99 ms	10 µs
	10 ms to 99.9 ms	100 µs
	100 ms to 999 ms	1 ms
	1 s to 10 s	10 ms